



Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 210067US2		SERIAL NO. 09/883,959	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Hidemasa ZAMA, et al.			
				FILING DATE June 20, 2001		GROUP 2814	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
<i>van</i>	AO	2000-197270	07/14/2000	JAPAN (with English Abstract)			X
<i>van</i>	AP	2000-295234	10/20/2000	JAPAN (with English Abstract)			X
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>van</i>	AW	"LOW-POWER HIGH-SPEED LSI CIRCUITS & TECHNOLOGY", chap. 2, Sec. 2.1.2.1., pages 60-75. MTCMOS Technology, (with English Abstract, pages 1-4) <i>No date</i>					
	AX						
	AY						
	AZ						
Examiner <i>Smith</i>					Date Considered <i>2/28/02</i>		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							